

Image Compression and Decompression (JPEG) LSI Chip Set*

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ible chip set (DCT engine, Huffman coders) developed by Kawasaki Steel.

man encoding in the Huffman encoder, it is output from the host bus with a header added. A memory card

Table 2 Features of scan format converter
KL5A71101

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- Conversion of raster-scan to block-scan and block-scan to raster-scan
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- Performs chrominance sampling rate conversion (YUV [4:2:2] - YUV [4:1:1])
 - Supports CCIR 601 format
 - Continuous throughput up to 27 MHz
 - Performs elastic buffering (1-line buffer)
 - JTAG (Joint Test Action Group) compliant
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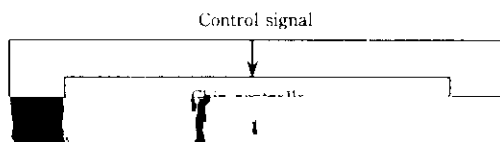


Table 3 Maximum number of pixel data

Color format	Max. number of pixel data/line
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